

REMARKS

Claims 13, 15, 17, and 20-22 are pending in this application. Claims 13, 15, and 22 are independent claims. Claims 17, 20, and 21 are dependent claims. Claims 1-12, 14, 16, 18, and 19 have been cancelled.

Claims 15, 17, and 20 have been allowed. Claims 13, 21, and 22 have been rejected. Amendments to claims 13 and 22 are presented herein. Claims 23-29 are newly added in this response. No new matter is being presented, and approval and entry are respectfully requested.

Rejections Under 35 U.S.C. § 102

In items 4 and 5 on pages 2 and 3 of the Office Action, the Examiner rejected claims 13, 21, and 22 under 35 U.S.C. § 102(b) as being anticipated by Biggs et al. (U.S. Patent No. 5,410,669). Applicants respectfully traverse these rejections for the reasons presented below.

Independent claim 13 recites, as amended, a method of controlling a cache memory, including "... assigning a second address space of the memory map, which is separate from the first address space of the main memory, for the cache memory when the cache memory is acting as the random access memory."

The Biggs reference teaches that when the instruction cache/SRAM module 14 operates in the SRAM mode, the base address retained in the base address register (BADDR) 41 is assigned.

In contrast, in the present invention, when the cache memory acts as the random access memory, a second address space of a memory map, which is separate from a first address space of the main memory, is assigned for the cache memory, as specified in claim 13. Thus, according to the present invention, it is possible to reduce circuit size without using hardware components such as the base address registers 41-44 in the cache memory of the Biggs reference.

Therefore, it is submitted that claim 13 patentably distinguishes over the prior art. Similar to claim 13, independent claim 22 recites, as amended, a computer, including "... an

assignment unit which assigns a second address space of the memory map, which is separate from the first address space of the main memory, for the cache memory when the cache memory is acting as the random access memory.” Thus, it is submitted that claim 22 patentably distinguishes over the prior art for at least the reasons presented above with respect to claim 13.

Dependent claim 21 depends from the independent claim 13 discussed above and is patentable over the prior art for at least the reasons discussed above.

Therefore, Applicants submit that claims 13, 21, and 22 patentably distinguish over the prior art. Accordingly, Applicants respectfully request reconsideration and withdrawal of the rejections under § 102.

New Claims

Claims 23-29 are newly added with this response to alternatively define the present invention. Similar to claim 13, independent claims 23 and 25 recite a system, including “... an assigning unit which assigns a second address space of the memory map, which is separate from the first address space of the main memory, for the cache memory when the cache memory is acting as the random access memory.” Also, independent claim 24 recites a method of controlling a computer, including “... assigning a second address space of the memory map, which is separate from the first address space of the main memory, for the cache memory when the cache memory is acting as a random access memory.”

These features are not taught or suggested by the cited reference. Thus, for at least the reasons presented above, Applicants submit independent claims 23-25 patentably distinguish over the prior art. Claims 26-29 are dependent claims and should be allowable for at least the reasons described above. Accordingly, Applicants respectfully request allowance of the new claims.

Conclusion

In accordance with the foregoing, it is respectfully submitted that all outstanding rejections have been overcome and/or rendered moot, and further, that all pending claims patentably distinguish over the prior art. Thus, there being no further outstanding rejections, the application is submitted to be in condition for allowance, which action is earnestly solicited.

If there are any formal matters remaining after this response, the Examiner is requested to telephone the undersigned to attend to these matters.

Finally, if there are any additional fees associated with filing of this response, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

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